



2025 International Conference on Analog VLSI Circuits (AVIC 2025)

Matsue, Shimane, October 20 – 22, 2025

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Important Dates

Paper Submission Deadline:

May 31, 2025

Author Notification Due:

August 8, 2025

Registration Starts:

August 11, 2025

Final Paper Submission Deadline:

September 10, 2025

Conference:

October 20– 22, 2025

Contact:

<https://avic2025.org/>

Organizers



IEE of Japan

Sponsors



IEEE SSCS
Japan Chapter



IEEE CASS
Japan Joint Chapter

Scope

The Research Committee on Electronic Circuits of the Institute of Electrical Engineers of Japan (IEEJ) will hold 2025 International Conference on Analog VLSI Circuits (AVIC) in Matsue, Japan on October 20 –22, 2025.

AVIC is the successor of International Analog VLSI Workshop, which had been held for 28 years. The purpose of this conference is to discuss state-of-the-art ideas and results from researches on analog VLSI circuits and their applications. It has contributed to long-standing worldwide community of analog VLSI circuit experts in industry and academia.

Topics

AVIC 2025 covers topics related to analog and mixed-signal integrated circuits including but not limited to:

• Analog and Mixed-signal Integrated Circuits:

Amplifiers, Filters, Comparators, Oscillators, Multipliers, PLLs, RF ICs, Voltage/Current References, Sample-And-Hold Circuits, A/D and D/A Converters, High Speed IO Interface, Energy Harvesting, Power Management

• Analog VLSI Design Automation:

Layout, Simulation, HDL, Analog IP

• Signal/Information Processing and Applications:

Communication, Multimedia, Automotive Electronics, Biomedical Electronics, Consumer Electronics, Non-linear Signal Processing, Neural Network, Sensing and Sensor Networks, Space Electronics

• Digital Circuits and Digital Signal Processing

Paper Submission

Paper submission is accepted via the official website <https://avic2025.org/> Authors are required to submit a full paper in English, in IEEE format, along with information including paper title, author name(s), affiliation(s) of author(s), and the corresponding author (name, postal and e-mail addresses). Notification will be sent to the authors via e-mail. Accepted papers must be resubmitted as the final (camera-ready) ones. At least one of the authors of a paper must register and give a presentation at the conference.

Visit AVIC 2025 website for additional information:

<https://avic2025.org/>

Accepted papers will be published by IEEE Computer Society Conference Publishing Services (CPS). Proceedings will be submitted for inclusion in IEEE Xplore.

Conference Venue

Kunibiki Messe (Matsue Convention Bureau)
1-2-1 Gakuen-minami, Matsue-shi, Shimane-ken, Japan